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	Subclass
Class	

PATENT NUMBER

U.S. UTILITY Patent Application

APPLICATION NO. 09/836286	CONT/PRIOR F	CLASS 438	SUBCLASS 706	ART UNIT/763 2533	EXAMINER Deo
APPLICANTS - Hidetaka Nambu		(Claim 1)			Fig. 40
TITLE Semiconductor device and manufacturing method of the device					

PTO-2040
12/00

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER		DRAWINGS		
<input type="checkbox"/> Sheets Drawn. <input type="checkbox"/> Figs. Drawn. <input type="checkbox"/> Print Fig.		<input type="checkbox"/> Top View <input type="checkbox"/> Side View <input type="checkbox"/> Bottom View <input type="checkbox"/> Front View <input type="checkbox"/> Back View <input type="checkbox"/> Top View		
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.		NOTICE OF ALLOWANCE AND ISSUE		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____		ISSUE DATE _____		
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<input type="checkbox"/> (Assignment Section) (D-1)		NOTICE OF ALLOWANCE AND ISSUE		
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